

ABSTRACT

Provided is an image decoding device which can eliminate block distortion and also can restrain decrease of a processing speed, even if an order of decoding blocks is changed.

Such an image decoding device (100) has: a variable length decoding unit (111) or the like which sequentially decodes the blocks; a frame memory (102); a de-blocking filtering unit (119) which applies filtering processing to each decoded block; switches (103 and 104) in the first executing status, which stores the block into the frame memory (102) every time the block is decoded, and applies the filtering processing to the stored blocks; switches (103 and 104) in the second executing status, which applies the filtering processing to the block to be applied with the filtering processing every time the block is decoded; and a control unit (101) which switches between the first executing status and the second executing status of the switches (103 and 104).